TOSHIBA TA31136FG/FNG

TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

# **TA31136FG,TA31136FNG**

# FM IF DETECTOR IC FOR CORDLESS TELEPHONE

Low operation voltage FM IF detector IC. This IC is suitable for cordless telephone.

#### **FEATURES**

Low operating voltage : V<sub>CC</sub> = 1.8~5.5V

Excellent temperature characteristics

• High sensitivity

12dB sensitivity : 11dB $\mu$ V EMF (Input 50 $\Omega$ )

• High intercept point : 96dB $\mu$ V (Input 50 $\Omega$ )

 Quadrature detector, both ceramic and coil discriminators are usable

Built-in 2nd MIX

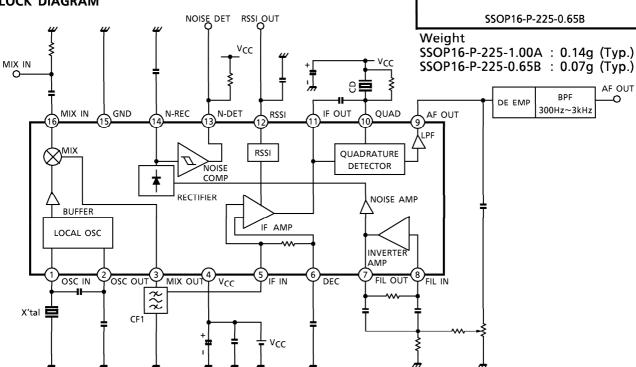
Operating frequency: 10~100MHz

• Built-in noise detection circuit

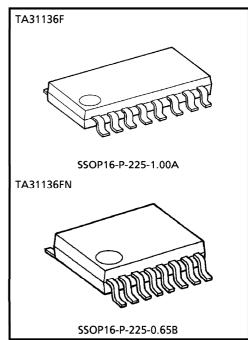
RSSI function

Very small package

#### **BLOCK DIAGRAM**



TA31136FNG Package is Pb-Free.



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# PIN FUNCTION (The values of resistor and capacitor are typical.)

PIN No.	PIN NAME	FUNCTION	INTERNAL EQUIVALENT CIRCUIT
1	OSC IN	Local oscillator input and output terminals. Colpitts oscillator is formed by internal emitter follower	1 VCC
2	OSC OUT	and external X'tal.  And external injection is possible from pin 2 or pin 1.	2 6pF
3	MIX OUT	MIX output terminal. Output impedance is around 1.8k $\Omega$ .	V <sub>CC</sub> V <sub>CC</sub> 3
4	V <sub>C</sub> C	Power supply	<del>-</del>
5	IF IN	2nd IF input and decoupling for bias. Input impedance is around	VCC VCC C C C C C C C C C C C C C C C C
6	DEC	1.8kΩ.	6 910Ω ⊕ §
7	FIL OUT	INVERTER AMP input and output terminals. BPF is composed of external	VCC 4 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0
8	FIL IN	capacitors and resistors. Connected internally to rectifier circuit by coupling capacitor.	8 500Ω C T 100Ω T
9	AF OUT	Demodulate signal output terminal. Carrier leak is small as LPF is built-in. Output impedance is around $360\Omega$ .	330Ω <b>3</b>

PIN No.	PIN NAME	FUNCTION	INTERNAL EQUIVALENT CIRCUIT
10	QUAD	Phase shift signal input terminal of FM demodulator.	νςς νςς
11	IF OUT	Output terminal of IF AMP.	V <sub>CC</sub> 100Ω 11
12	RSSI	This terminal outputs DC level according to input signal level to IF AMP. Dynamic range is around 70dB.	\$\frac{13}{8} \\ \frac{13}{8}
13	N-DET	The result of noise detection is output by comparing output voltage of N-REC terminal with internal refrence. Hysteresis range is about 100mV and output is open collector.	(3)
14	N-REC	After output of INVERTER AMP amplified around 20dB, noise signal is rectified by external capacitor.	VCC NOISE COMP
15	GND	GND terminal.	_
16	MIX IN	1st IF signal input terminal. Input impedance is around $4k\Omega$ at 21.7MHz.	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$

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#### **DESCRIPTION**

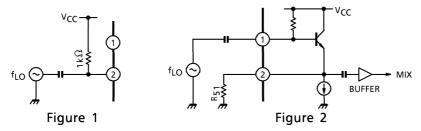
#### 1. Local oscillator external injection method

Inject as shown in Figure 1, setting the injection level between  $95 dB \mu V$  and  $100 dB \mu V$ . A built-in BUFFER amp. minimizes leakage from the mixer.

Input from pin 1 is possible as shown in Figure 2. However, when the input frequency is high, the level at pin 2 may not be sufficient, causing a decrease in sensitivity.

In such a case, add resistor  $R_{51}$  and set the input signal so that signal level at pin 2 is  $95{\sim}100 dB \mu V$ .

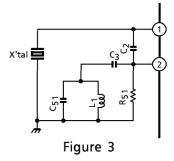
The input capacitance of pins 1 and 2 is respectively 1.5pF (typ.) and 4.6pF (typ.).



#### 2. Overtone oscillation

Figure 3 shows the basic configuration of the local oscillation circuit using overtone oscillation. The  $C_{51}$  and  $L_1$  tuning circuits prevent crystal fundamental oscillation. Therefore, set  $C_{51}$  and  $L_1$  to inductive at the fundamental frequency and capacitive at the overtone frequency.

Since the level at pin 2 may decrease and the sensitivity may fall at high frequency as with external injection, adjust the oscillation level using R<sub>51</sub>.



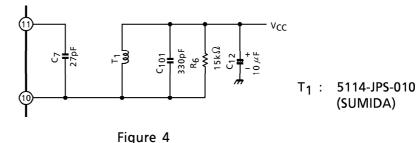
## 3. Detection circuit

Detection stage is quadrature method.

Oscillator is ceramic discriminator on reference application. In case of using coil, connect as shown in Figure 4. In this case, demodulation output  $V_{\text{OD}}$  is about  $80\text{mV}_{\text{rms}}$ . Demodulation output can be increased by raising damping resistance  $R_3$ . However, be careful because the temperature dependency of the modulation output also increases.

Center frequency  $f_0$  and demodulation output depends largely on phase shifter and  $C_7$ . For  $C_7$ , use a capacitor with good temperature characteristics.

In case of coil, especially  $C_{101}$ , use a capacitor with good temperature characteristics.



## 4. Demodulation output distortion factor

Demodulation output distortion factor is about -43dB when ceramic discriminator CDB450C24 used, is about -50dB when coil 5114-JPS-010 used. (IF  $100dB\mu V$  EMF input, measured pin 9 before when input from MIX demodulation output distortion factor depends largely on a ceramic filter band and a group delay characteristic. Select ceramic filter adequately.

### 5. INVERTER AMP usage

The INVERTER AMP can be used to form a band pass filter as shown in Figure 4. Set constants as in equations (1) to (3). However, because a low pass filter and a high pass filter are built in, it is recommended that center frequency  $f_0$  be about 30kHz.

(1) 
$$f_0 = \frac{1}{2\pi\sqrt{R_3(R_4//R_5)C^2}}$$

(2) 
$$G_V = R_3 / 2R_4$$

(3) 
$$Q^2 = \frac{R_3}{4(R_4//R_5)}$$

Example R<sub>3</sub> = 150k
$$\Omega$$
, R<sub>4</sub> = 330k $\Omega$ , R<sub>5</sub> = 3.3k $\Omega$ , Rp = 20k $\Omega$  (VR) C = 220pF provide ; f<sub>0</sub>  $\simeq$  31kHz, G<sub>V</sub>  $\simeq$  -13dB Q  $\simeq$  12

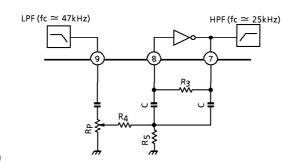


Figure 5

#### 6. Noise detection rise time

The rise time is a proportion of time constant 7.5ms of the smoothing capacitor  $C_9 = 0.1 \mu F$  of the noise rectifier and internal resistor 75k $\Omega$ . Although decreasing the capacitance of  $C_9$  can shorten the rise time, note that the noise detection output fluctuation may increase. This should be taken into account before use.

#### 7. RSSI function

A DC voltage corresponding to the input level of IF input pins (pin 5) is output to the RSSI pin (P21). While the linear range is about 80dB when  $V_{CC} = 2V$ , the range can be expanded to 80dB as in Figure 6.

However, in such a case, note that the temperature characteristics of the RSSI output may alter due to a disparity between the temperature coefficient of the external resistor and the internal resistance of the IC.

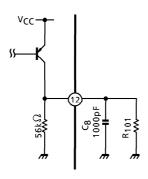
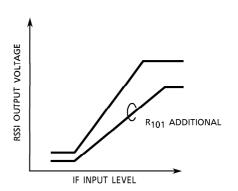


Figure 6



8. DC voltages for pins (Typical values for reference)

 $V_{CC} = 2.0V$ 

PIN No.	PIN NAME	VOLTAGE	PIN No.	PIN NAME	VOLTAGE
1	OCS IN	1.98	9	AF OUT	_
2	OSC OUT	1.33	10	QUAD	2.0
3	MIX OUT	0.74	11	IF OUT	1.14
4	V <sub>CC</sub>	2.0	12	RSSI	_
5	IF IN	1.67	13	N-DET	_
6	DEC	1.67	14	N-REC	_
7	FIL OUT	0.67	15	GND	0.0
8	FIL IN	0.65	16	MIX IN	0.94

(UNIT: V)

## **MAXIMUM RATINGS** (Ta = 25°C)

CHARAC	CTERISTIC	SYMBOL	RATING	UNIT		
Supply Voltage	9	Vcc	7	V		
Power	TA31136F	D-	370	mW		
Dissipation	TA31136FN	PD	560	11100		
Operating Tem	perature	T <sub>opr</sub>	<b>- 30∼85</b>	°C		
Storage Tempe	erature	T <sub>stg</sub>	<b>- 50∼150</b>	°C		

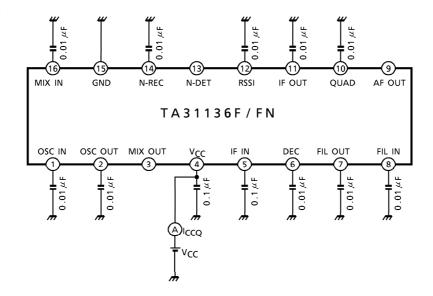
## **ELECTRICAL CHARACTERISTICS**

(Unless otherwise specified,  $V_{CC} = 2.0V$ ,  $f_{IN} (MIX) = 21.7MHz$ ,  $f_{IN} (IF) = 450kHz$ ,  $\Delta f = \pm 1.5kHz$ ,  $f_{MOD} = 1kHz$ ,  $Ta = 25^{\circ}C$ 

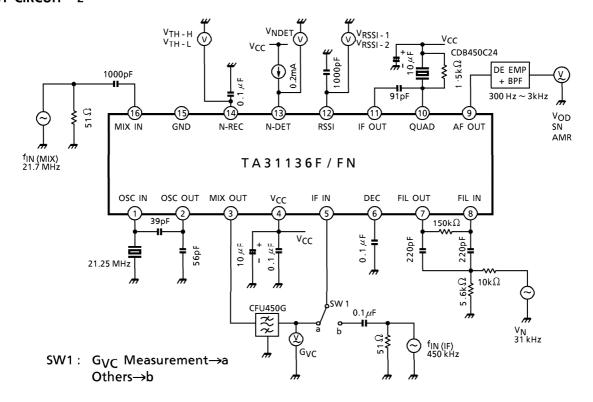
CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Power Supply Voltage	V <sub>C</sub> C	—	_	1.8	2.0	5.5	V
Current Consumption	lccQ	1	_	_	3.2	4.6	mA
Mixer Conversion Gain	G <sub>VC</sub>	2	Measured through ceramic filter. $VIN (MIX) = 46dB \mu V$	15	18	21	dB
Mixer Intercept Point	PIM	_	Input $50\Omega$	_	96	_	$dB\muV$
Mixer Input Impedance	RIN (MIX)	<b>—</b>		_	5.5	_	kΩ
Input Impedance	CIN (MIX)	_	<u> </u>	_	2.8	_	pF
Mixer Output Resistance	Ro (MIX)		_	1.2	1.8	2.4	kΩ
12dB Sensitivity	12dB SN	_	_	_	11	_	$dB\muV$
Demodulation Output Level	V <sub>OD</sub>	2	V <sub>IN (IF)</sub> = 80dBμV	70	100	130	mV <sub>rms</sub>
SN Ratio	SN	2	$V_{IN (IF)} = 80 dB \mu V$	43	65	_	dB
AM Rejection Ratio	AMR	2	$V_{IN}$ (IF) = 80dB $\mu$ V, AM = 30%	_	40	_	dB
IF AMP. Input Resistance	R <sub>IN</sub> (IF)	—	_	1.2	1.8	2.4	kΩ
RSSI Output Voltage	V <sub>RSSI-1</sub>	2	$V_{CC} = 3V$ $V_{IN} (IF) = 30 dB \mu V$	200	360	520	mV
K33i Output Voltage	V <sub>RSSI-2</sub>	2	$V_{IN}(IF) = 100 dB \mu V$	1.4	2.0	2.6	V
Noise Detection Output Voltage	V <sub>NDET</sub>	2	I SINK = 0.2mA	_	0.1	0.5	V
Noise Detection Output Leak Current	ILEAK		V <sub>NREC</sub> = 0.6V, V <sub>NDET</sub> = 2V	_	0	5	μΑ
Noise "H" Level	V <sub>TH-H</sub>	2			0.5	0.7	V
Detection Level "L" Level	V <sub>TH-L</sub>		_	0.3	0.4	_	v

All AC levels are indicated by open level (EMF).

#### TEST CIRCUIT 1

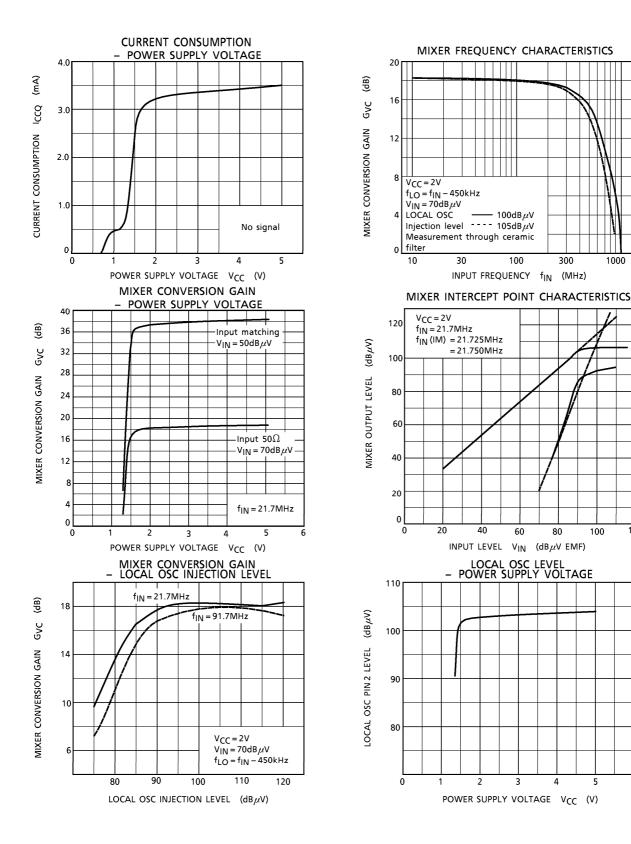


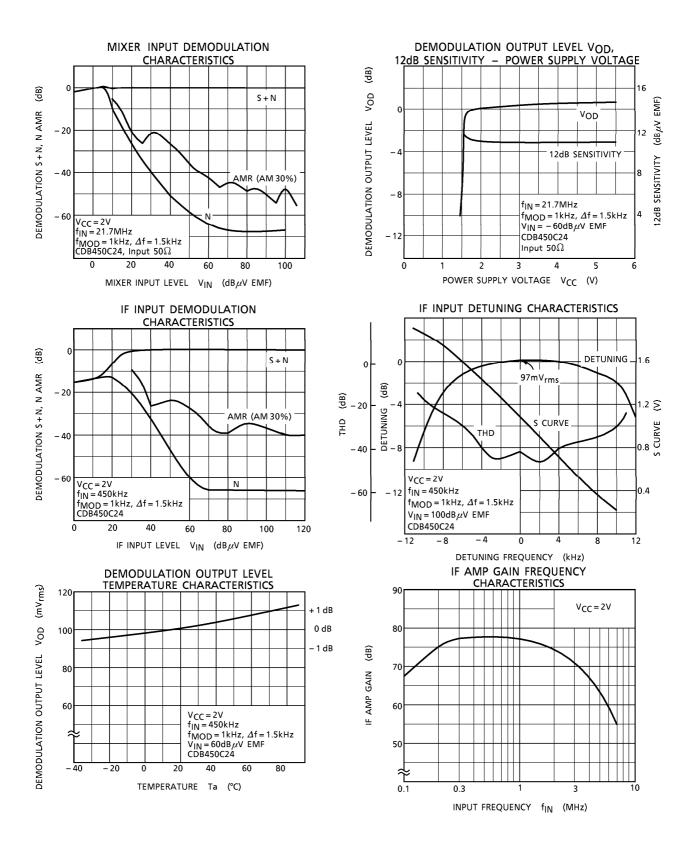
## **TEST CIRCUIT** 2



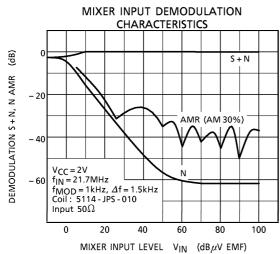
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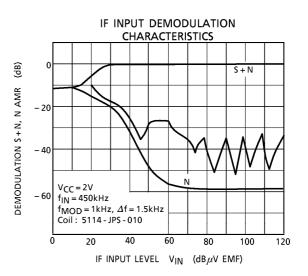
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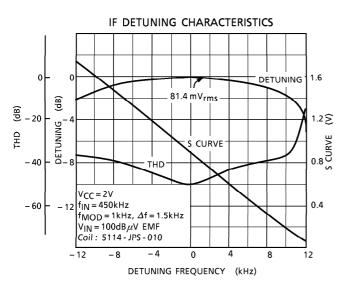


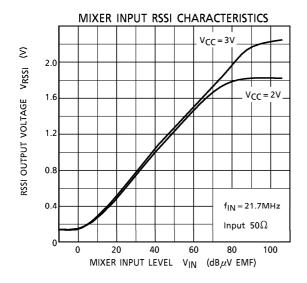


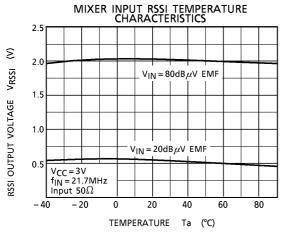
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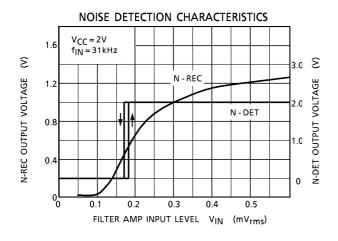


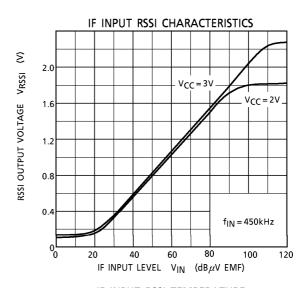


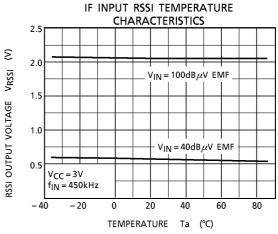


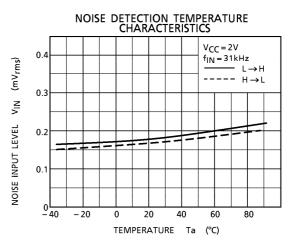


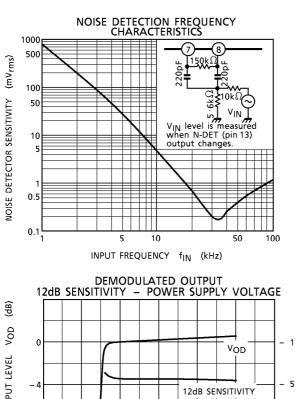


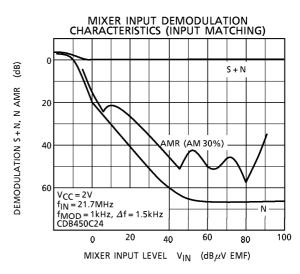


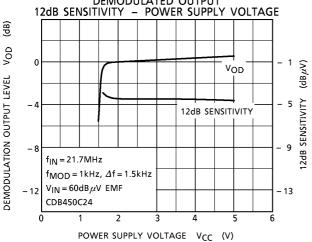


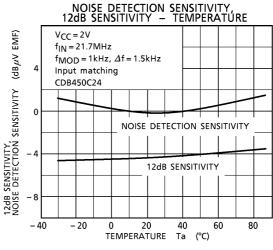


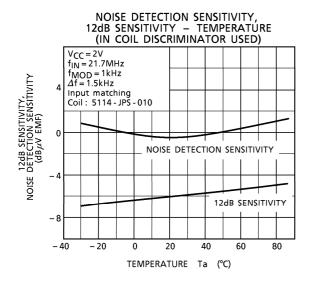


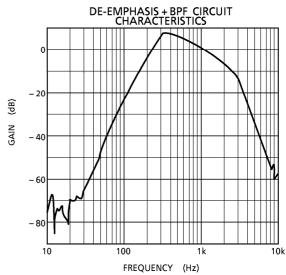




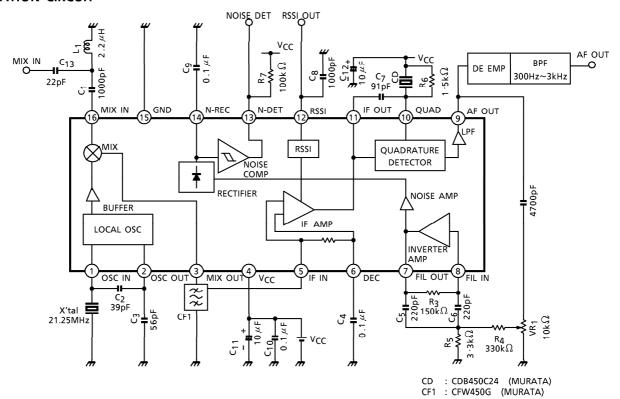








#### **APPLICATION CIRCUIT**

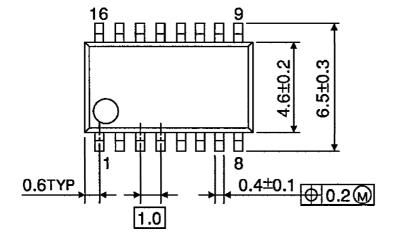


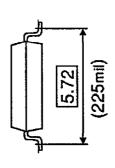
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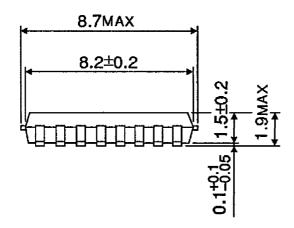
Unit: mm

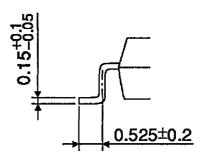
# **PACKAGE DIMENSIONS**

SSOP16-P-225-1.00A





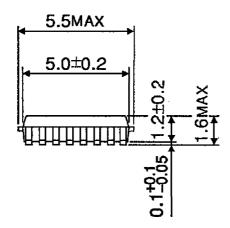


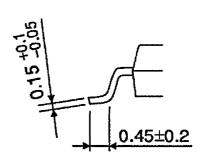


Weight: 0.14g (Typ.)

Unit: mm

# PACKAGE DIMENSIONS SSOP16-P-225-0.65B





Weight: 0.07g (Typ.)

Notice for Pb free product
About solderability, following conditions were confirmed
Solderability

- (1) Use of Sn-63Pb solder bath
   Solder bath temperature = 230
  - <u>Dipping time</u> = 5seconds
  - The number of times = once
  - · Use of R-type flux
- (2) Use of Sn-3.0Ag-0.5Cu solder bath
  - Solder bath temperature = 245
  - Dipping time = 5seconds
  - The number of times = once
  - · Use of R-type flux

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000707EBA

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